

<b>Notice of References Cited</b>	Application/Control No. 10/066,984	Applicant(s)/Patent Under Reexamination YEH ET AL.	
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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
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**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Patterson and Hennessy, "Computer Architecture: A Quantitative Approach," Second Edition, Morgan Kaufmann Publishers, Inc., 1996, pages 230 & 242-251.
	V	Johnson, Mike. Superscalar Microprocessor Design. Prentice Hall, 1991. Page 18.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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